

**PATENT**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:	)
	)
Chen, Addison	) Art Unit: 2133
	)
Application No. 10/783,345	) Examiner: Alphonse, Fritz
	)
Filed: 02/20/2004	) Atty. Docket No.
	) NVIDP341/P001326
For: SYSTEM AND METHOD FOR	)
GENERATING 128-BIT CYCLIC	) Date: February 9, 2007
REDUNDANCY CHECK VALUES WITH	)
<u>32-BIT GRANULARITY</u>	)

**AMENDMENT A**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Examiner:

In response to the Office Action mailed 11/09/2006, please enter the following amendments believed to place the claims in condition for allowance.